

FIG. 1B

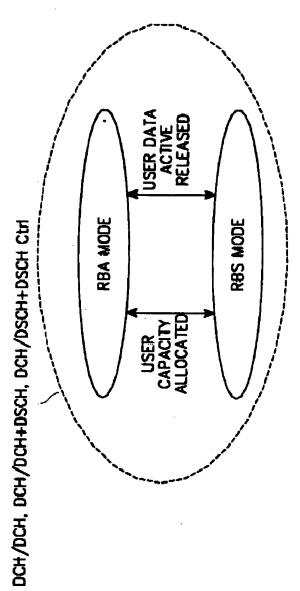
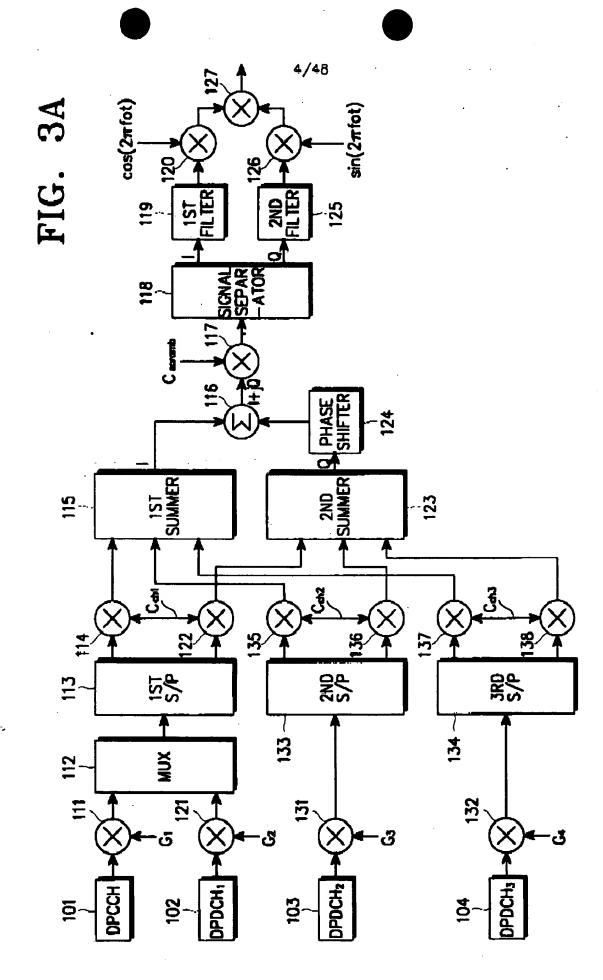


FIG. 2A

DATA1 TPC TFCI DATA2 PILOT ATA BITS NALOT BITS	*			נייטרט	ע חייים א
	DATA1	TPC Nine BITS	TFCI Nee BITS	DATA2 Npara BITS	PILOT Negat BITS

FIG. 2B

DATA Ndata BITS	NTT BITS NTB BITS NTC BITS  10*2* BITS(k=06)
	FBI NR BITS S(k=06)
	1FCI NITE BITS PS, 10*2* BIT
	PILOT   TFC  FB  NP. BITS   NP.
ОРОСН	ОРОСН
211	213



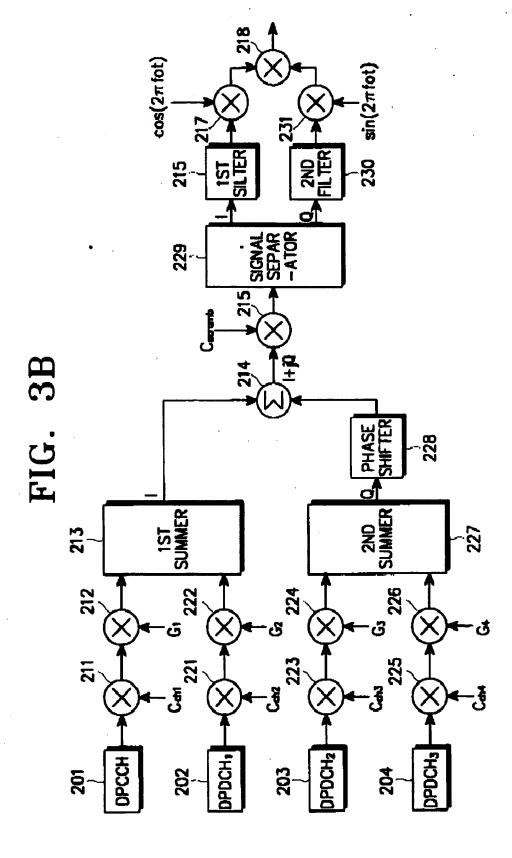


FIG. 4A

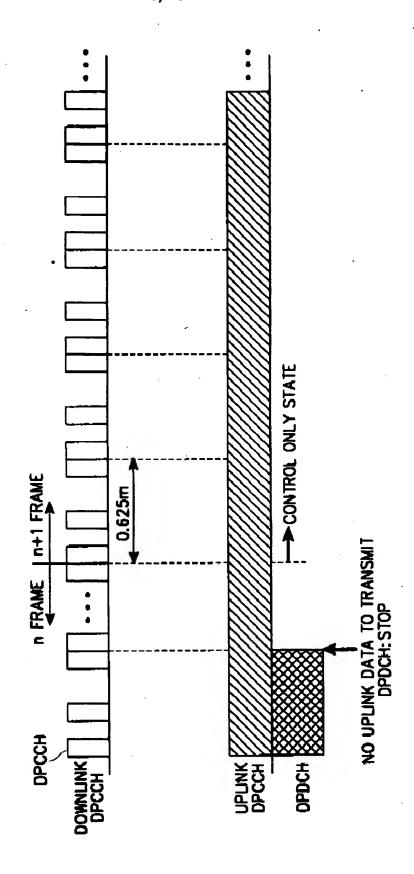
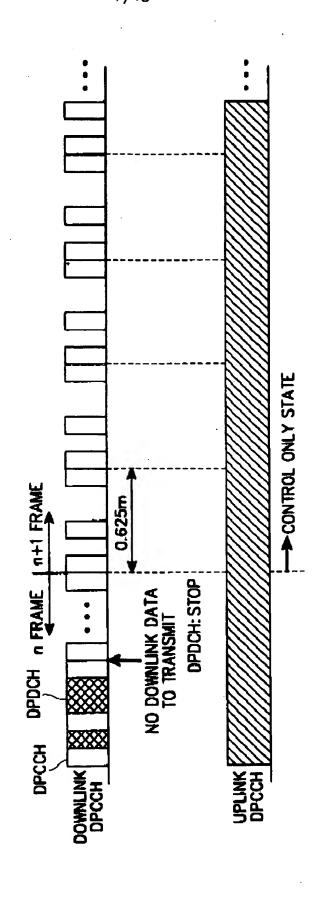
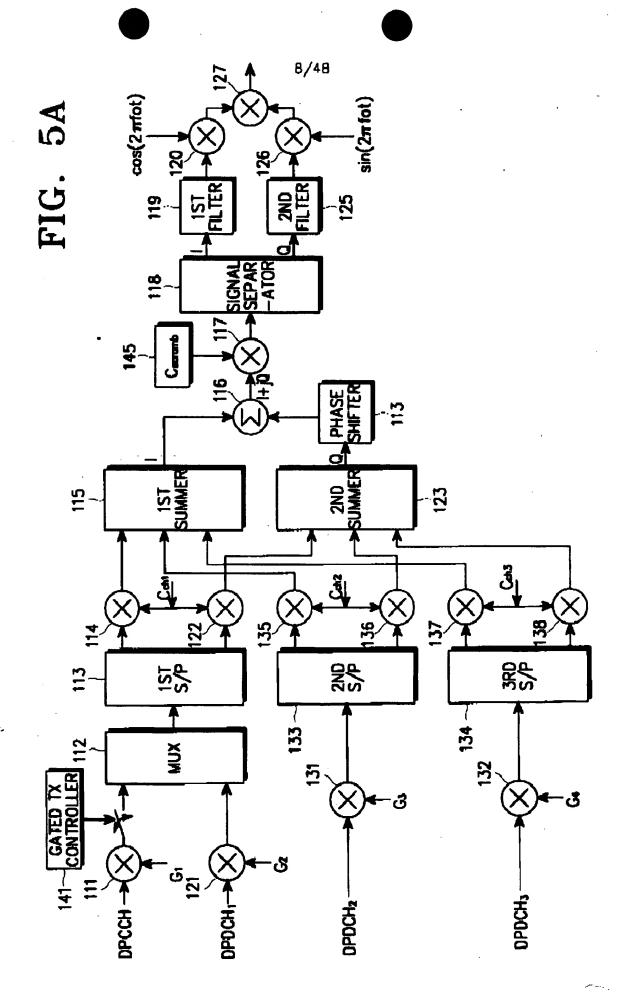
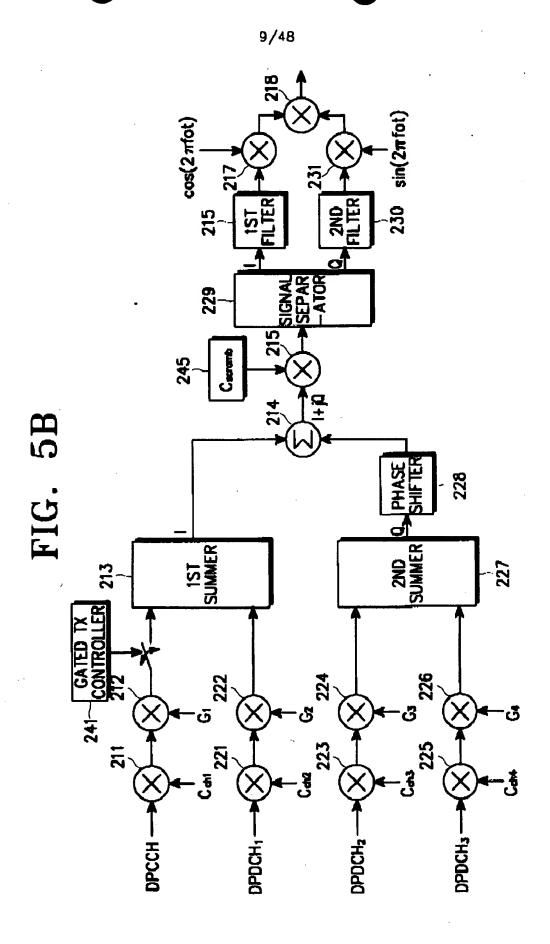
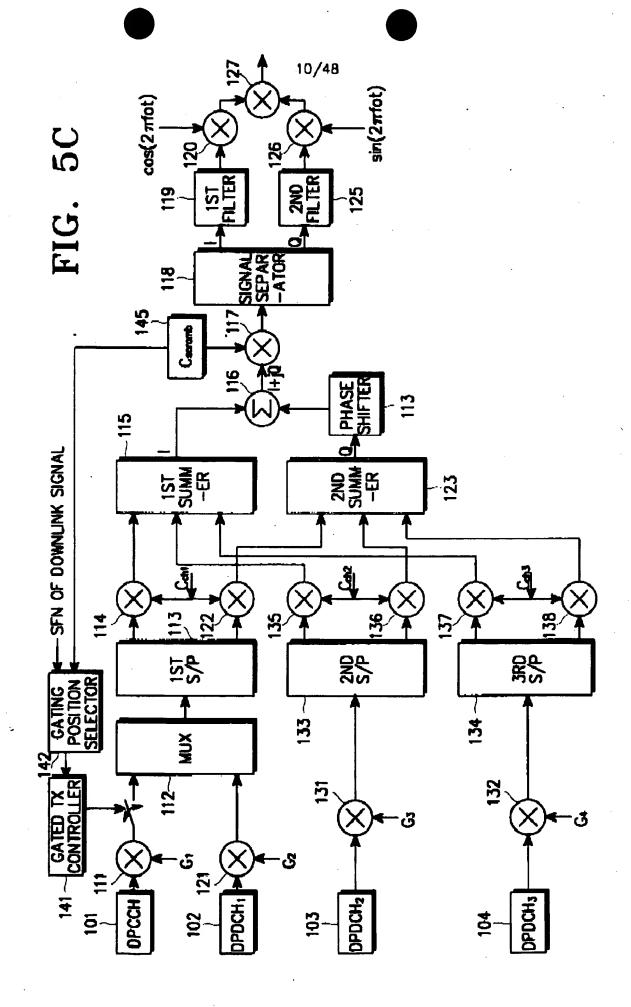


FIG. 4B









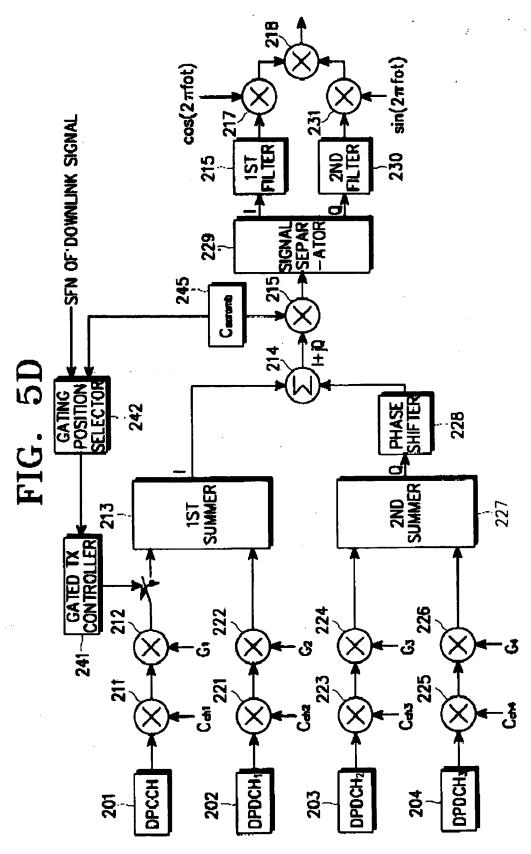


FIG. 6A

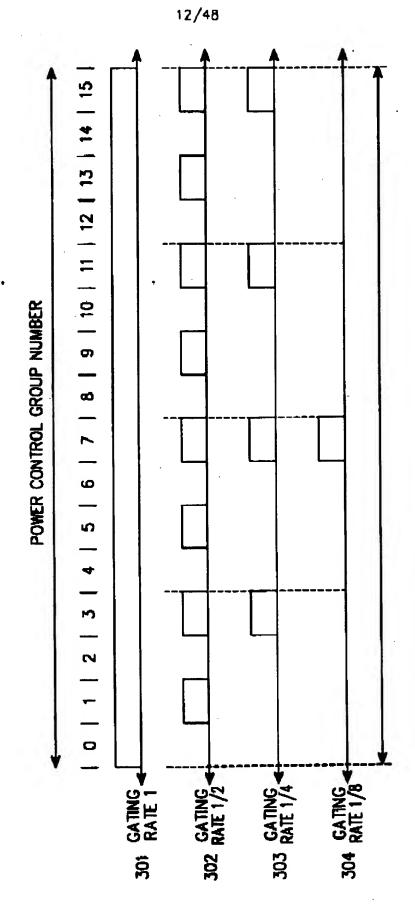


FIG. 6B

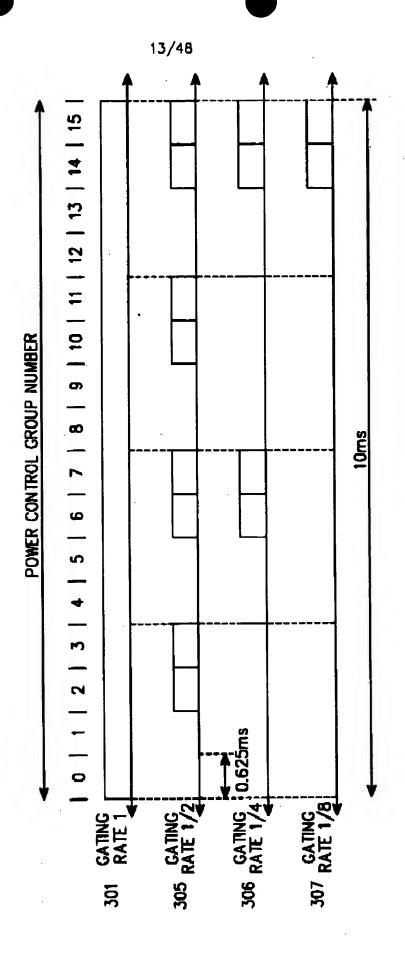


FIG. 7A

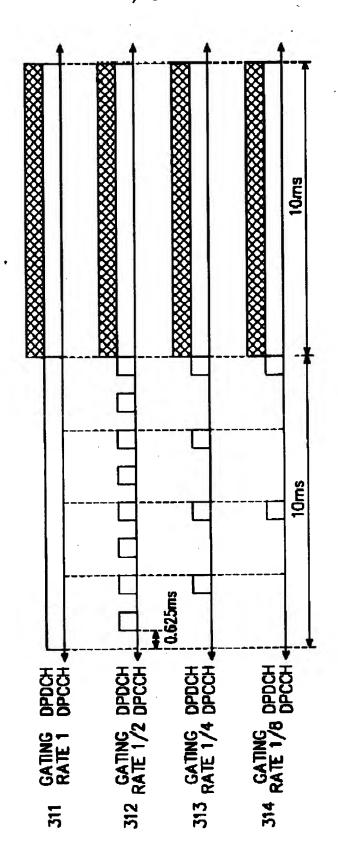
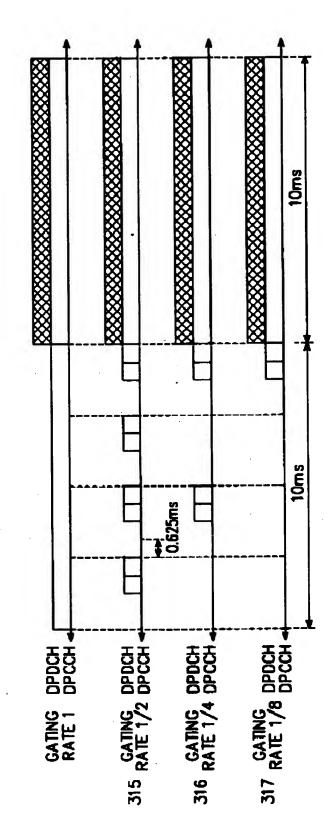


FIG. 7B



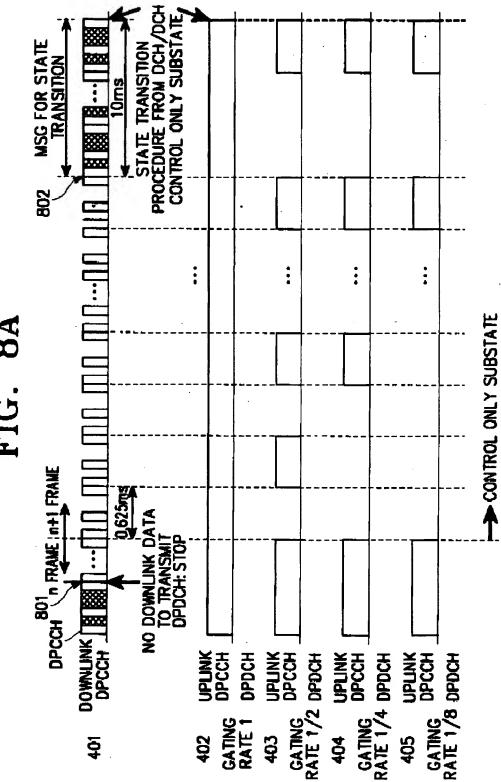
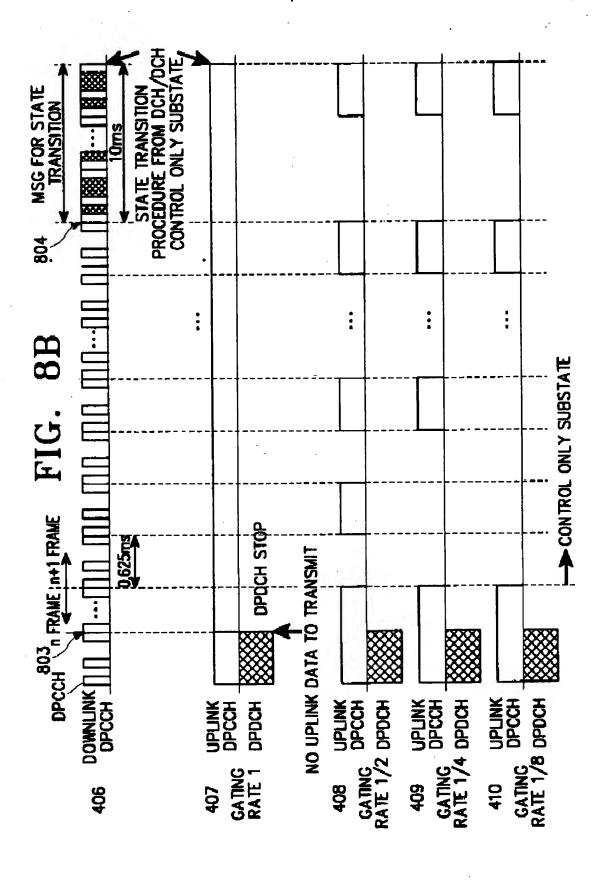
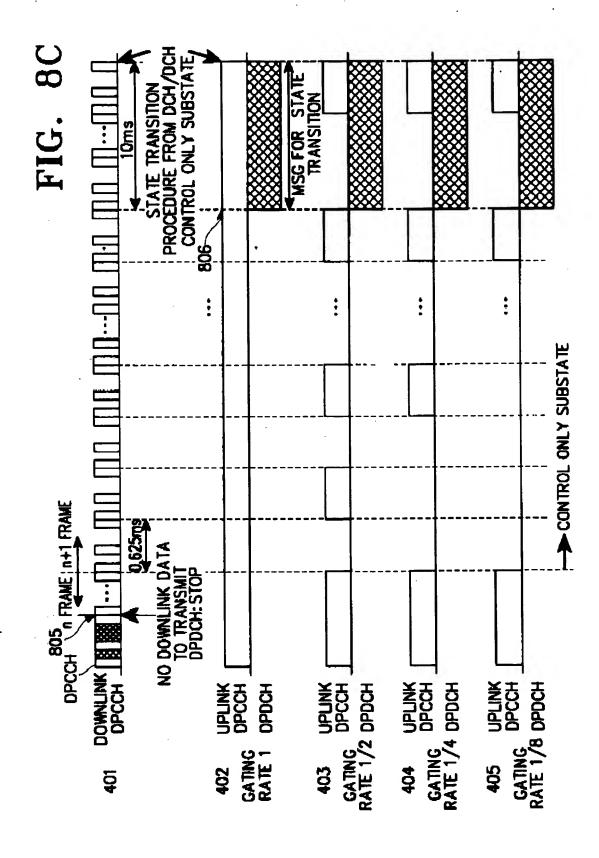


FIG. 8A





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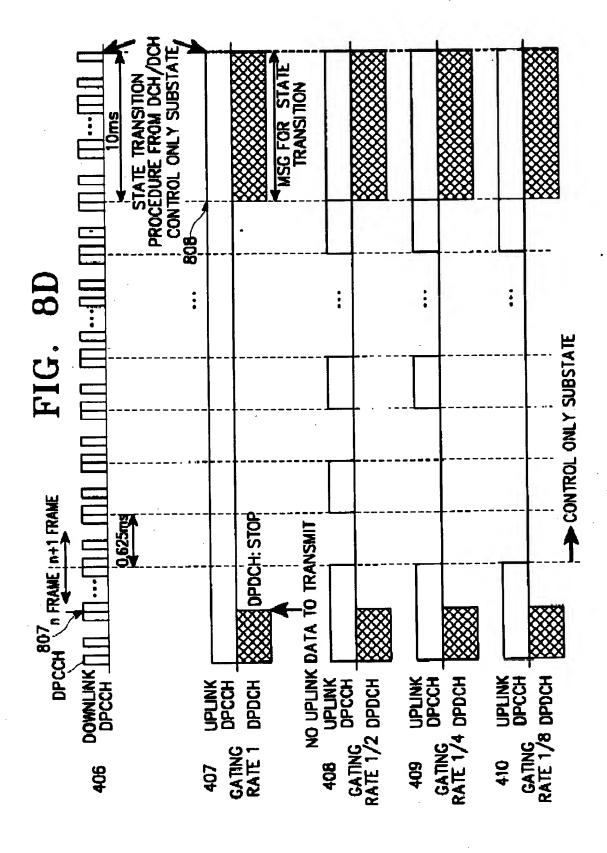


FIG. 9A

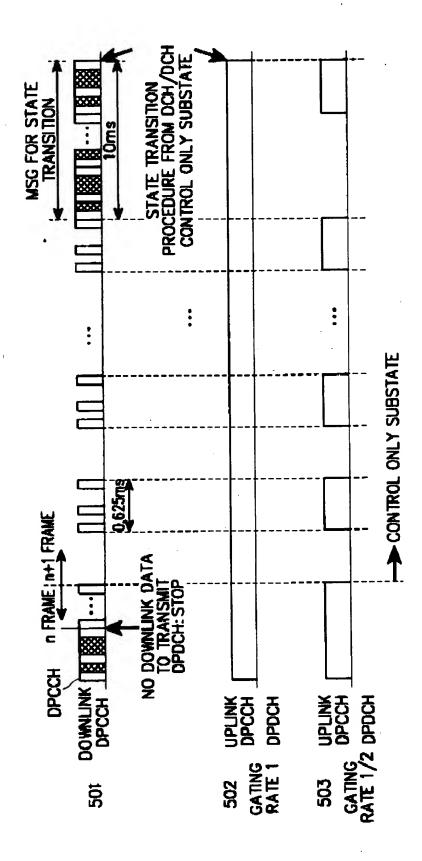
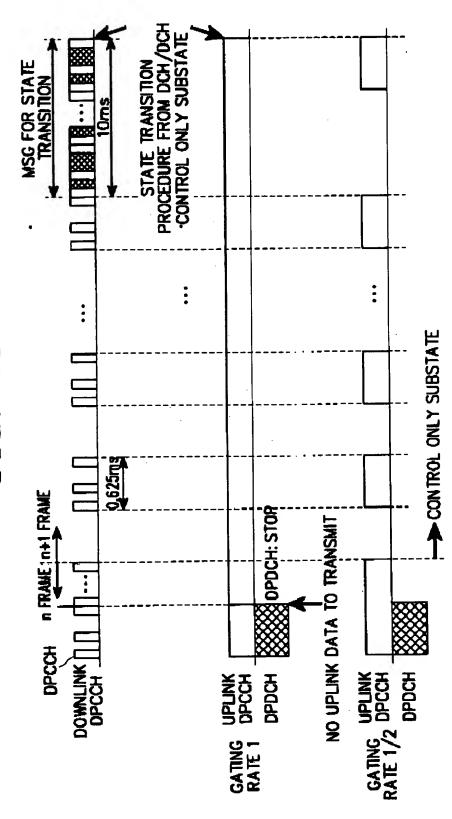
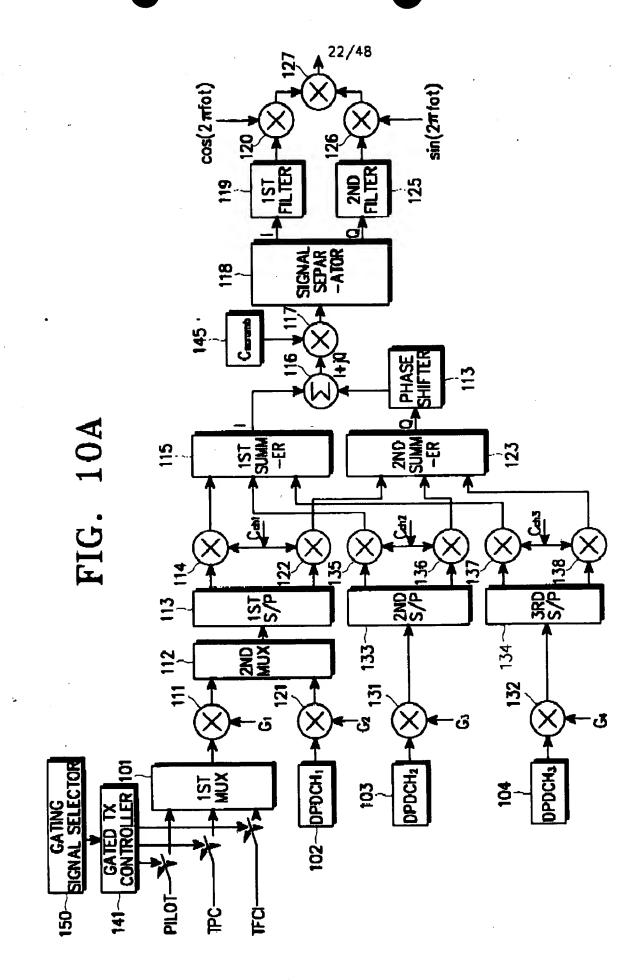
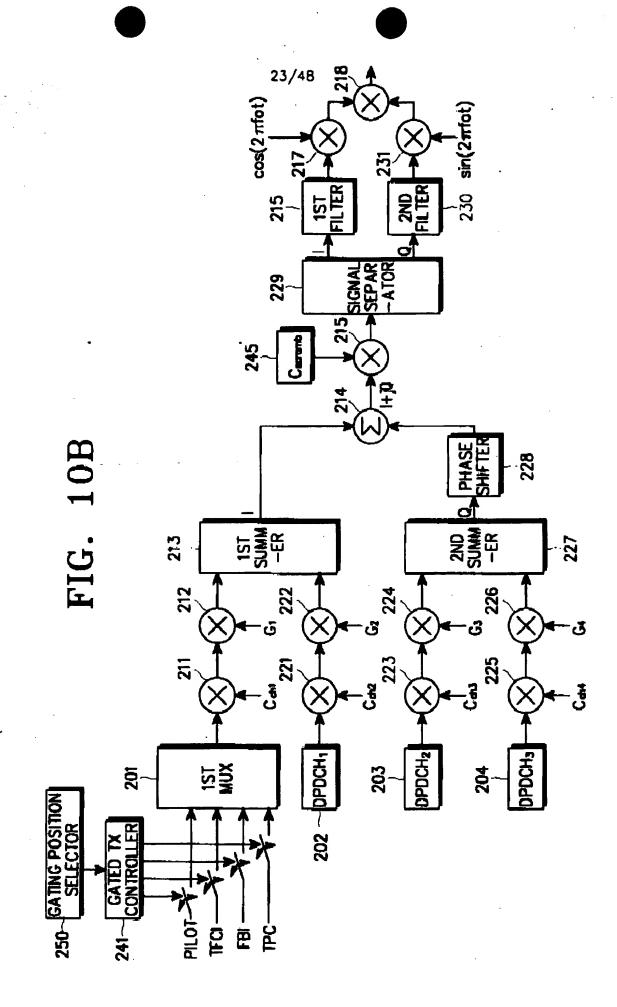


FIG. 9B







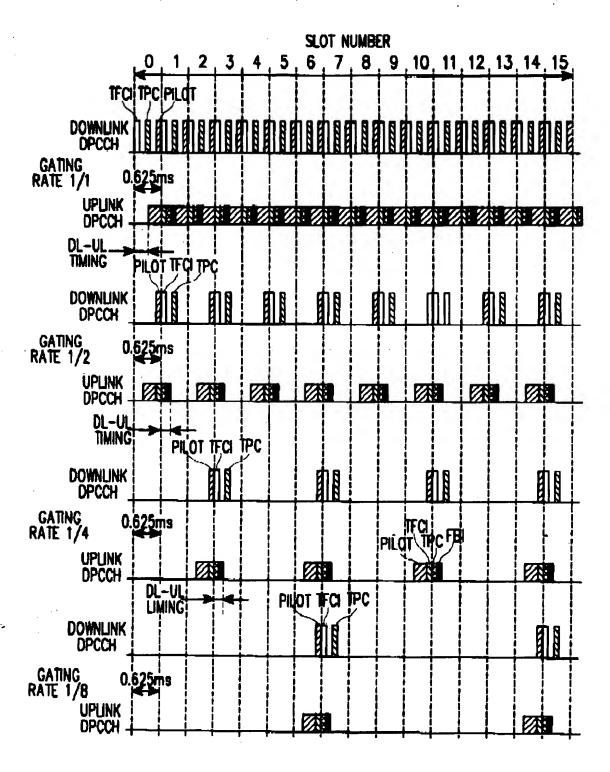


FIG. 11A

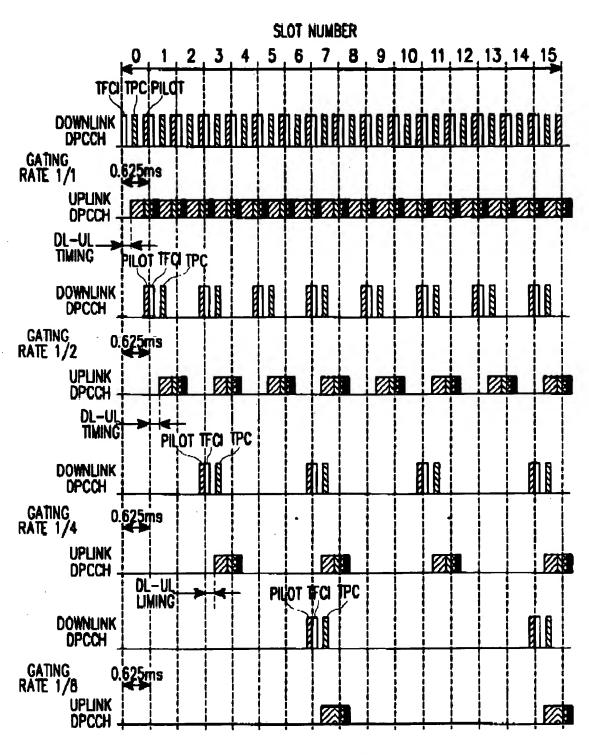


FIG. 11B

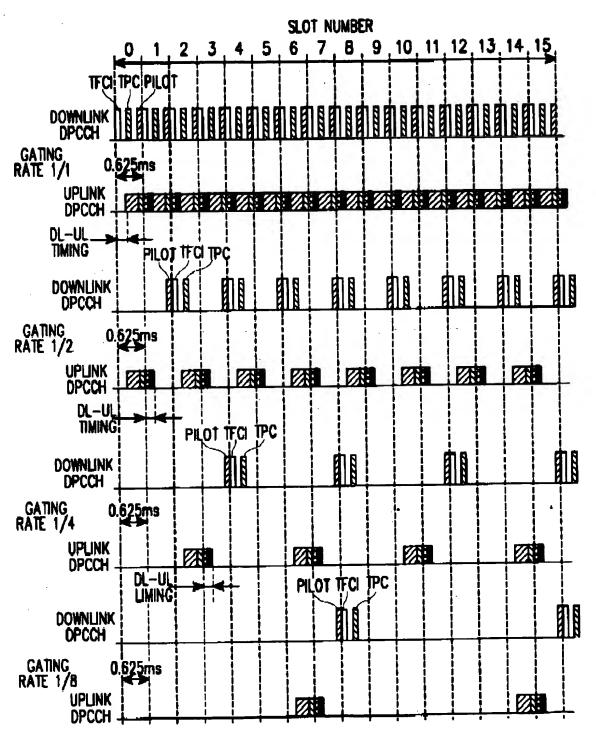


FIG. 11C

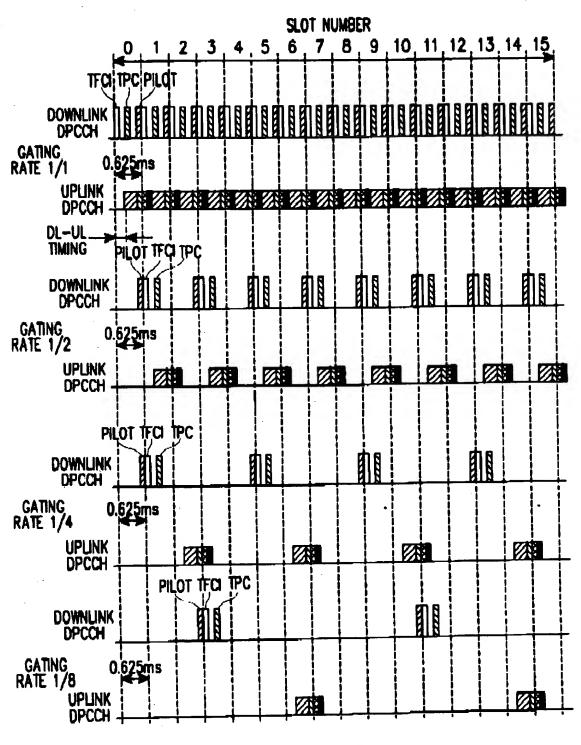


FIG. 11D

FIG. 12A

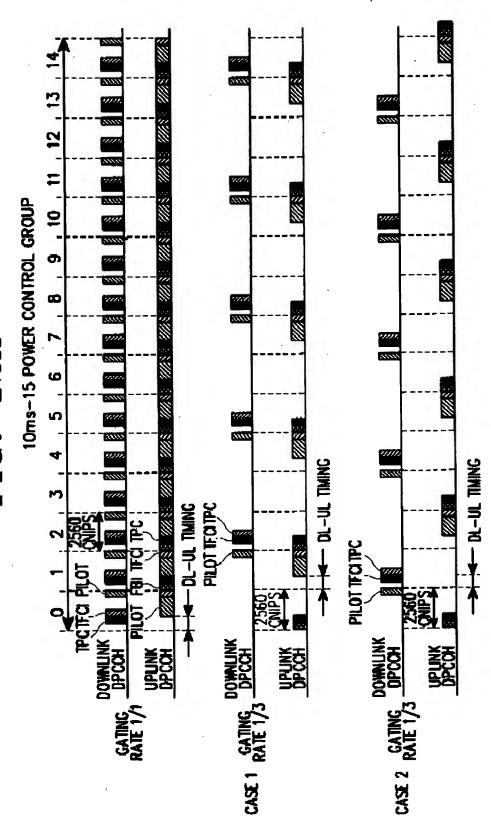


FIG. 12B

10ms-15POWER CONTROL GROUP .

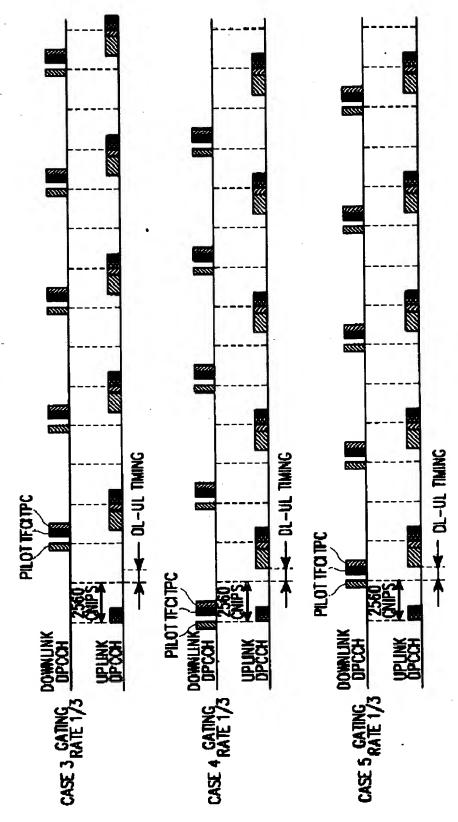


FIG. 12C

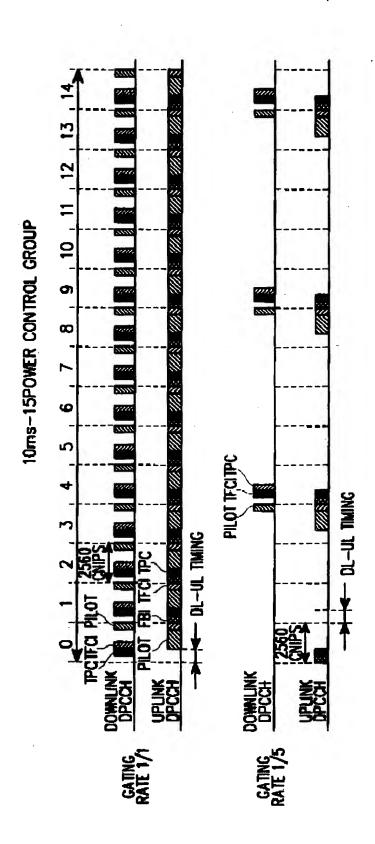


FIG. 12D

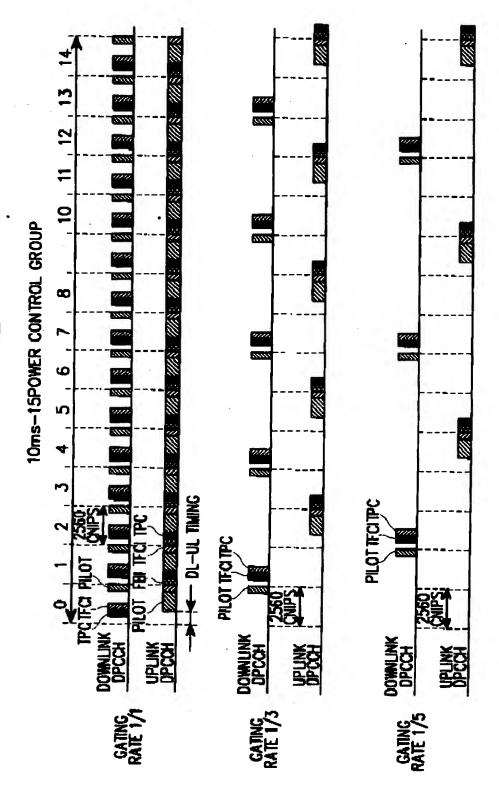
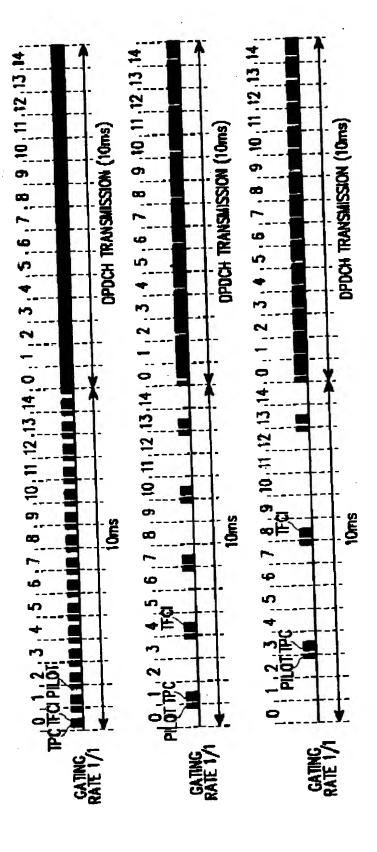


FIG. 12E

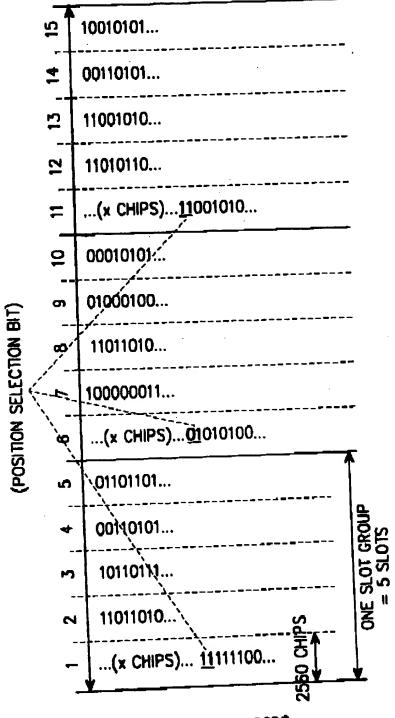


10010101... 5 10ms=15 POWER CONTROL GROUP x = SFN\*m (0<m<36) 00110101... 11010110. 00010101. 9 01000100 **6**0,4 (POSITION SELECTION BIT) 10000011... (x CHIPS)...00001010...

REAL PART OF SCRAMBLING CODE

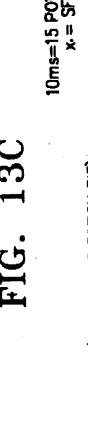
CATING RATE 1/3 FIG. 13B

10ms=15 POWER CONTROL GROUP x = SFN\*m (0<m<36)



REAL PART OF SCRAMBLING CODE

CATING PATE 1/5



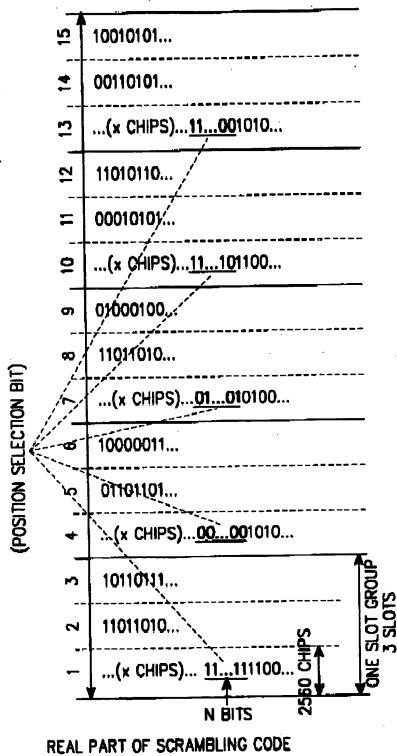
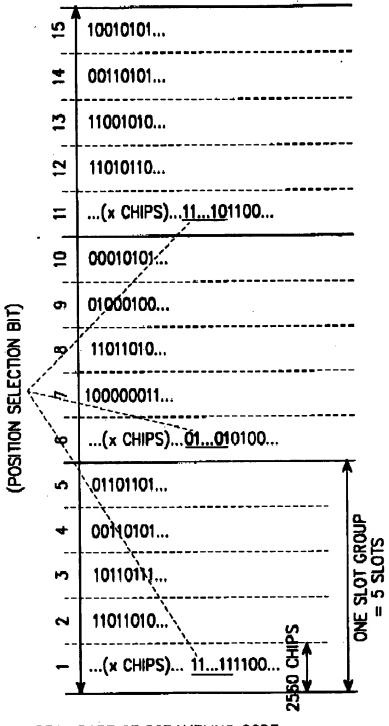


FIG. 13D

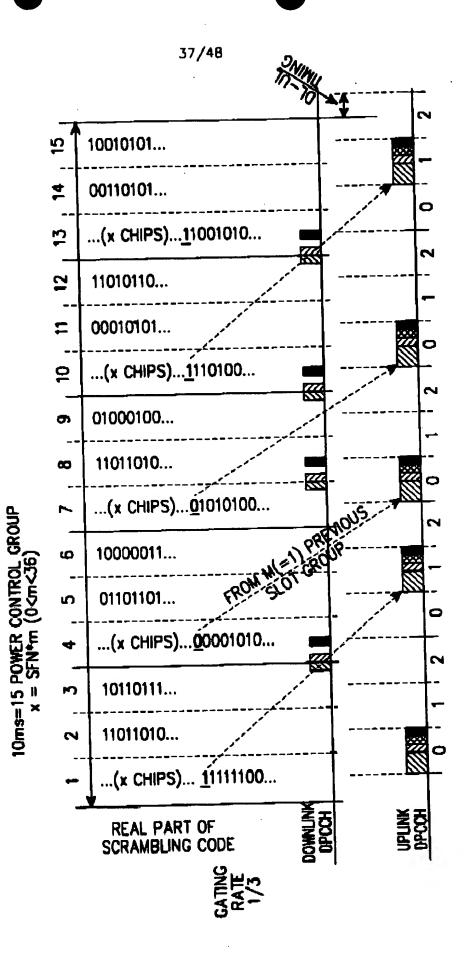
10ms=15 POWER CONTROL GROUP x = SFN\*m (0<m<36)



**REAL PART OF SCRAMBLING CODE** 

GATING RATE 1/5

FIG. 14A



10010101... M 00110101.. 2 11001010... 11010110... 2 ...(x CHIPS)...<u>11</u>001010... 10ms=15 POWER CONTROL GROUP x = SFN\*m (0<m<36) 00010101... 9 01000100... G) 11011010... œ 100000011... 0 ...(x CHIPS)...<u>01</u>010100... 01101101... S M 00110101... 10110111... M 11011010... 2 0 .(x CHIPS)... <u>11</u>111100... UPCOH OPCOH REAL PART OF SCRAMBLING CODE

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FIG. 14B

2 10010101...  $\overline{\mathbf{c}}$ 00110101... 7 0 ...(x CHIPS)...<u>11...00</u>1010..  $\mathbf{C}$ 2 11010110... 12 00010101... ...(x CHIPS)...<u>11...10</u>1100... õ 01000100... 6 11011010.  $\boldsymbol{\varpi}$ ...(x CHIPS)...<u>01...01</u>0100... 10ms=15 POWER CONTROL GROUP x = SFN\*m (0<m<36) 10000011... φ 01101101.. S 0 ...(x CHIPS)...<u>00...00</u>1010.. ~ 10110111.. 11011010.. 2 ...(x CHIPS)... 11...111100... N BITS REAL PART OF SCRAMBLING CODE

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FIG. 14C

FIG. 14D 10ms=15 POWER CONTROL GROUP  $x = SFN^*m$  (0cm<36)

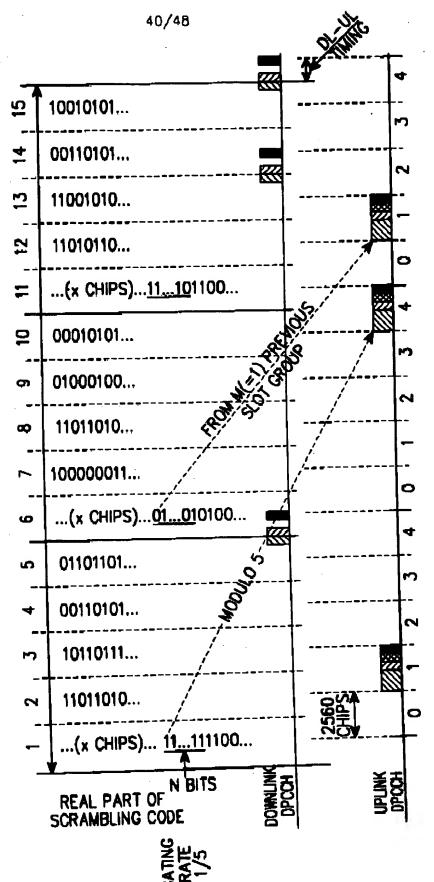


FIG. 15A

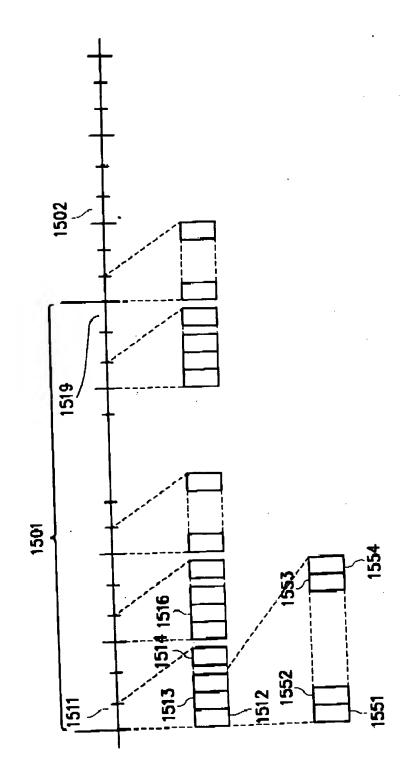


FIG. 15B

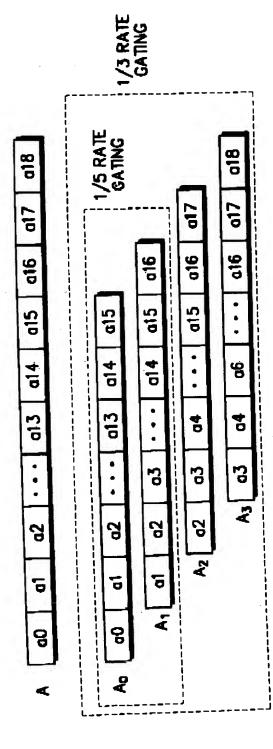
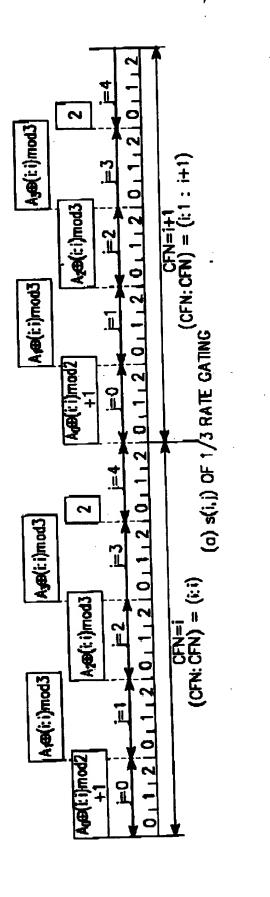


FIG. 15C



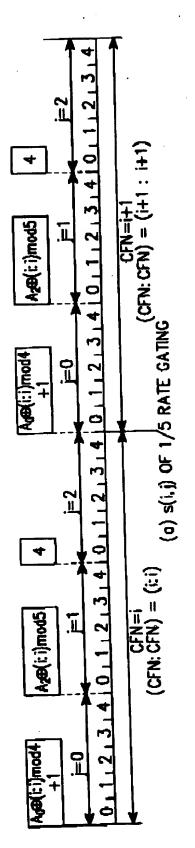
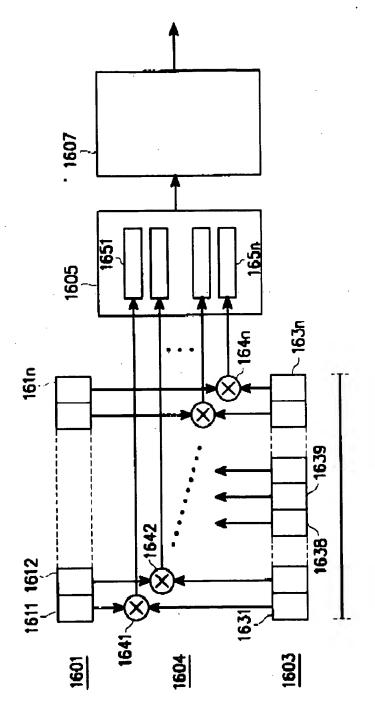


FIG. 16



TX POWER CONTROL POINT

FIG. 17B

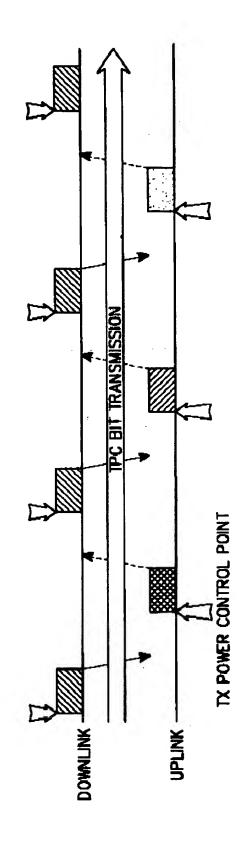


FIG. 18A

